Extending CSP with tests for availability

Gavin Lowe

Oxford University Computing Laboratory, Wolfson Building, Parks Road, Oxford, OX1 3QD, UK; e-mail gavin.lowe@comlab.ox.ac.uk

Abstract. We consider the language of CSP extended with a construct that allows processes to test whether a particular event is available (without actually performing the event). We present an operational semantics for this language, together with two congruent denotational semantic models. We also show how this extended language can be simulated using standard CSP, so as to be able to analyse systems using the model checker FDR.

Keywords. CSP, tests for availability, semantic models.

1. Introduction

Many languages for message-passing concurrency allow programs to test whether a channel is ready for communication, without actually performing that communication. For example, in JCSP [WBM⁺07,WB09], the input and output ends of channels have a method pending(), to test whether there is data ready to be read, or whether there is a reader ready to receive data, respectively. Java InputStreams have a method available() that returns the number of bytes that are available to be read. Andrews [And00] gives a number of examples using such a construct.

In this paper, we study the effect of adding such tests to the process algebra CSP [Ros97]. In particular, we add a single new construct to the language: the process

if ready a then P else Q

(1)

tests whether the event a is ready for communication, and then acts like either P or Q, appropriately. More precisely, the process tests whether all other processes that have a in their alphabet (and so who must synchronise on a) are ready to perform a.

We assume that within constructs of the form of (1), the test for the readiness of a is carried out only once: if the event becomes available or unavailable after the test is performed, that does not affect the branch that is selected. We allow processes to test for the readiness of events outside their own alphabet.

In this paper we investigate the effect of adding the construct (1) to semantic models for CSP. In the next section, we give a brief overview of the syntax and standard semantics of CSP. In Section 3 we give some examples using the new construct, both to illustrate its potential usefulness, and to highlight some implications for the semantic models. In Section 4 we give an operational semantics to the language; then in Section 5 we give congruent denotational models, analogous to the traces and stable failures models of CSP [Ros97]. In Section 6 we show how this extended language can be simulated using standard CSP, so as to be able to analyse systems using a model checker such as FDR [Ros94,For97]. We sum up in Section 7.

2. A brief overview of CSP

In this section we give a brief overview of the syntax of CSP; for simplicity and brevity, we consider a fragment of the language in this paper. We also give a brief overview of the traces and stable failures models of CSP. For more details, see [Hoa85,Ros97].

CSP is a process algebra for describing programs or *processes* that interact with their environment by communication. Processes communicate via atomic events, from some set Σ . Events are often passed on channels; for example, the event *c*.3 represents the value 3 being passed on channel *c*. The notation $\{|c|\}$ represents the set of events over channel *c*.

The simplest process is *STOP*, which represents a deadlocked process that cannot communicate with its environment. The process div represents a divergent process that can only perform internal events.

The process $a \to P$ offers its environment the event *a*; if the event is performed, it then acts like *P*. The process $c?x \to P$ is initially willing to input a value *x* on channel *c*, i.e. it is willing to perform any event of the form *c*.*x*; it then acts like *P* (which may use *x*).

A standard conditional is written as if *b* then *P* else *Q*, where *b* is a boolean condition on variables within the process (such as variables that hold values previously input)¹. The process b & P is equivalent to if *b* then *P* else *STOP*: *P* is enabled only if the boolean guard *b* is true. For convenience, we extend this notation to readiness tests and define:

ready a & P as shorthand for if ready a then P else STOP, notReady a & P as shorthand for if ready a then STOP else P.

The tests now act as guards upon P, so that P can be performed only if a is available or not available, respectively.

The process $P \Box Q$ can act like either P or Q, the choice being made by the environment: the environment is offered the choice between the initial events of P and Q. By contrast, $P \Box Q$ may act like either P or Q, with the choice being made internally, and not under the control of the environment. The process $P \triangleright Q$ represents a sliding choice or timeout: the process initially acts like P, but if no event is performed then it can internally change state to act like Q.

The process $P_A||_B Q$ runs P and Q in parallel; P is restricted to performing events from A; Q is restricted to performing events from B; the two processes synchronise on events from $A \cap B$. In this paper we will take the alphabets A and B to comprise just standard events, as opposed to actions corresponding to readiness tests. As noted above, we allow processes to test for the readiness of events outside their alphabets, e.g.

 $(\operatorname{ready} b \And a \to STOP)_{\{a\}} \|_{\{b\}} \ (b \to STOP \sqcap STOP).$

In examples, we will tend to omit the alphabets when they are clear from the context.

The process $P \parallel Q$ runs P and Q in parallel, synchronising on events from A. The process

 $P \parallel Q$ interleaves $\stackrel{\sim}{P}$ and Q, i.e. runs them in parallel with no synchronisation.

The process $P \setminus A$ acts like P, except the events from A are hidden, i.e. turned into internal, invisible events, denoted τ , which do not need to synchronise with the environment.

The process P[R] represents *P* where events are renamed according to the relation *R*, i.e., P[R] can perform an event *b* whenever *P* can perform an event *a* such that *a R b*. The relation *R* is often presented as a substitution; for example P[b/a, c/a] represents *P*, with the event *a* renamed to both *b* and *c*, and all other events unchanged.

¹We use the same syntax for both standard conditionals and readiness tests, but they are semantically different constructs.

Recursive processes may be defined equationally, or using the notation $\mu X \bullet P$, which represents a process that acts like *P*, where each occurrence of *X* represents a recursive instantiation of $\mu X \bullet P$.

Prefixing (\rightarrow) and guarding (&) bind tighter than each of the binary choice operators, which in turn bind tighter than the parallel operators.

CSP can be given both an operational and denotational semantics. The denotational semantics can either be extracted from the operational semantics, or defined directly over the syntax of the language; see [Ros97]. It is more common to use the denotational semantics when specifying or describing the behaviours of processes, although most tools act on the operational semantics.

A *trace* of a process is a sequence of (visible) events that a process can perform. We write traces(P) for the traces of P. If tr is a trace, then $tr \upharpoonright A$ represents the restriction of tr to the events in A, whereas $tr \setminus A$ represents tr with the events from A removed; concatenation is written " $^{~}$ "; A^* represents the set of traces with events from A.

A *stable failure* of a process *P* is a pair (tr, X), which represents that *P* can perform the trace *tr* to reach a stable state (i.e. where no internal events are possible) where *X* can be refused, i.e., where none of the events of *X* is available. We write *failures*(*P*) for the stable failures of *P*.

3. Examples

In this section we consider a few examples, firstly to illustrate the usefulness of the new construct, and then to highlight some aspects of the semantics.

Being able to detect readiness on channels can be useful in a number of circumstances. For example, the construct:

 $a \to P$ \Box notReady $a \& b \to Q$

gives priority to a over b: the event b can be performed only if the environment is not willing to perform a (at the point at which the test is made). Note, though, that if the environment withdraws its willingness to communicate a after the notReady a test is performed, then the above construct will be blocked, even if b is available: the construct makes the assumption about the environment that a is not withdrawn in this way.

As a slightly larger example, consider the classic readers and writers problem [CHP71]. Here collections of readers and writers share a database. In order to maintain consistency, readers may not use the database at the same time as writers, and at most one writer may use the database at a time. The following guard process supports this: readers (resp. writers) gain entry to the database by performing the event *startRead* (resp. *startWrite*) and perform *endRead* (resp. *endWrite*) when they are finished. The parameters *r* and *w* record the number of readers and writers currently using the database, and satisfy the invariant $w \le 1 \land (r > 0 \Rightarrow w = 0)$.

Guard(r, w) = $w = 0 \& startRead \rightarrow Guard(r + 1, w)$ $\Box endRead \rightarrow Guard(r - 1, w)$ $\Box r = 0 \land w = 0 \& startWrite \rightarrow Guard(r, w + 1)$ $\Box endWrite \rightarrow Guard(r, w - 1).$

The problem with the above design is that writers may be permanently locked out of the database if there is always at least one reader using the database (even if no individual reader uses the database indefinitely). The following version gives priority to writers, by not allowing a new reader to start using the database if there is a writer waiting:

 $\begin{array}{l} Guard(r,w) = \\ w = 0 \& \text{ notReady } startWrite \& startRead \rightarrow Guard(r+1,w) \\ \Box \ endRead \rightarrow Guard(r-1,w) \\ \Box \ r = 0 \land w = 0 \& \ startWrite \rightarrow Guard(r,w+1) \\ \Box \ endWrite \rightarrow Guard(r,w-1). \end{array}$

This idea can be extended further, to achieve fairness to both types of process; the parameter *priRead* records whether priority should be given to readers.²

 $\begin{array}{l} Guard(r,w,priRead) = \\ w = 0 \land priRead \& startRead \rightarrow Guard(r+1,w,false) \\ \Box w = 0 \& \text{ notReady } startWrite \& startRead \rightarrow Guard(r+1,w,false) \\ \Box endRead \rightarrow Guard(r-1,w,false) \\ \Box r = 0 \land w = 0 \land \neg priRead \& startWrite \rightarrow Guard(r,w+1,true) \\ \Box r = 0 \land w = 0 \& \text{ notReady } startRead \& startWrite \rightarrow Guard(r,w+1,true) \\ \Box endWrite \rightarrow Guard(r,w-1,true). \end{array}$

We now consider a few examples in order to better understand aspects of the semantics of processes with readiness tests: it turns out that some standard algebraic laws no longer hold. Throughout these examples, we omit alphabets from the parallel composition operator where they are obvious from the context.

Example 1 Consider $P \parallel Q$ where $P = a \rightarrow STOP$ and Q = if ready *a* then $b \rightarrow STOP$ else *error* $\rightarrow STOP$. Clearly, it is possible for *Q* to detect that *a* is ready and so perform *b*. Could *Q* detect that *a* is not ready, and so perform *error*? If *P* makes *a* available immediately then clearly the answer is no. However, if it takes *P* some time to make *a* available, then *Q* could test for the availability of *a* before *P* has made it available.

We believe that any implementation of prefixing will take some time to make *a* available: for example, in a multi-threaded implementation, scheduling decisions will influence when the *a* becomes available; further, the code for making *a* available will itself take some time to run. This is the intuition we follow in the rest of the paper. This decision has a considerable impact on the semantics: it will mean that *all* processes will take some time to make events available (essentially since all the CSP operators maintain this property).

Returning to Example 1, in the combination $P \parallel Q$, Q can detect that a is not available initially and so perform *error*.

Example 2 (if ready *a* then *P* else *Q*) \setminus {*a*} = *P* \setminus {*a*}: the hiding of *a* means that the ready *a* test succeeds, since there is nothing to prevent *a* from happening.

Example 3 External choice is not idempotent. Consider $P = a \rightarrow STOP \sqcap b \rightarrow STOP$ and $Q = \text{ready } a \& \text{ ready } b \& error \rightarrow STOP$. Then $P \parallel Q$ cannot perform *error*, but $P \square P \parallel Q$ can, if the two nondeterministic choices are resolved differently.

We do not allow external choices to be resolved by ready or notReady tests: we consider these tests to be analogous to evaluation of standard boolean conditions in if statements, or boolean guards, which are evaluated internally.

Example 4 The process $R = \text{ready } a \& P \Box$ notReady a & Q is not the same as if ready a then P else Q, essentially since the former checks for the readiness of a twice, but the latter checks

²In principle one could merge the first two branches, by using a guard $w = 0 \land (priRead \lor notReady startWrite)$; however, allowing complex guards that mix booleans with readiness testing would complicate the semantic definitions.

only once. When in the presence of the process $a \to STOP$, R can evolve to the state $P \Box Q$ (if the ready a test is made after the a becomes available, and the notReady a test is made before the a becomes available) or to $STOP \Box STOP = STOP$ (if the ready a test is made before the a becomes available, and the notReady a test is made after the a becomes available, and the notReady a test is made after the a becomes available, and the notReady a test is made before the a becomes available, and the notReady a test is made after the a becomes available, and the notReady a test is made after the a becomes available, and the notReady a test is made after the a becomes available); neither of these is, in general, a state of if ready a then P else Q.

Example 5 ready a & ready b & P is not the same as ready b & ready a & P. Consider $P = error \to STOP$ and $Q = a \to STOP \triangleright b \to STOP$. Then ready $a \& ready b \& P \parallel Q$ can perform *error*, but ready $b \& ready a \& P \parallel Q$ cannot. Similar results hold for notReady, and for a mix of ready and notReady guards.

The above example shows why we do not allow more complex guards, such as ready $a \land$ ready b & P: any natural implementation of this process would have to test for the availability of a and b in some order, but the order in which those are tested can make a difference.

4. Operational semantics

In this section we give operational semantics to the language of CSP extended with tests for the readiness or non-readiness of events. For simplicity, we omit interleaving, the \parallel form of

parallel composition, and renaming from the language we consider.

As normal, we write $P \xrightarrow{a} P'$, for $a \in \Sigma \cup \{\tau\}$ (where Σ is the set of visible events, and τ represents an internal event), to indicate that P performs the event a to become P'. In addition, we include transitions to indicate successful readiness or non-readiness tests:

- We write $P \xrightarrow{ready a} P'$ to indicate that P detects that the event a is ready, and evolves into P';
- We write $P \xrightarrow{notReady a} P'$ to indicate that P detects that the event a is not ready, and evolves into P'.

Note the different fonts between ready and notReady, which are part of the syntax, and *ready* and *notReady*, which are part of the semantics.

Define, for $A \subseteq \Sigma$:

$ready A = \{ready a \mid a \in A\},\$	$notReady A = \{notReady a \mid a \in A\},\$
$A^{\dagger} = A \cup readyA \cup notReadyA,$	$A^{\dagger au} \!=\! A^{\dagger} \cup \{ au\}.$

Transitions, then, will be labelled by elements of $\Sigma^{\dagger \tau}$. We think of the $\xrightarrow{ready a}$ and $\xrightarrow{notReady a}$ transitions as being internal in the sense that they cannot be directly observed by any parallel peer. We refer to elements of $\Sigma^{\dagger \tau}$ as *actions*, and restrict the word *events* to elements of Σ^{τ} .

Below we use standard conventions, writing, e.g., $P \xrightarrow{a}$ for $\exists P' \bullet P \xrightarrow{a} P'$, and $P \not\xrightarrow{a}$ for $\neg (\exists P' \bullet P \xrightarrow{a} P')$.

Recall our intuition that a process such as $a \to P$ may not make the *a* available immediately. We model this by a τ transition to a state where the *a* is indeed available. It turns out that this latter state is not expressible within the syntax of the language (this follows from Lemma 11, below). Within the operational semantic definitions, we will write this state as $\check{a} \to P$. We therefore define the semantics of prefixing by the following two rules.

$$a \to P \xrightarrow{\tau} \check{a} \to P, \qquad \qquad \check{a} \to P \xrightarrow{a} P.$$

We stress, though, that the $\check{a} \rightarrow \ldots$ notation is only for the purpose of defining the operational semantics, and is not part of the language.

The following rules for normal events are completely standard. For brevity, we omit the symmetrically equivalent rules for external choice (\Box) and parallel composition ($_A \parallel_B$). The identifier *a* ranges over *visible* events.

$P \xrightarrow{a} P'$		$P \xrightarrow{\tau} P'$
$P \Box Q \overset{a}{\longrightarrow} P'$		$P \Box Q \xrightarrow{\tau} P' \Box Q$
$\begin{array}{c} P \xrightarrow{a} P' \\ \hline P \triangleright Q \xrightarrow{a} P' \end{array}$		$\begin{array}{c} P \xrightarrow{\tau} P' \\ \hline P \triangleright O \xrightarrow{\tau} P' \triangleright O \end{array}$
$P \triangleright Q \xrightarrow{\tau} Q$	$P \sqcap Q \stackrel{\tau}{\longrightarrow} P$	$P \sqcap Q \xrightarrow{\tau} Q$
$\frac{P \stackrel{\alpha}{\longrightarrow} P'}{P_A \ _B Q \stackrel{\alpha}{\longrightarrow} P'_A \ _B Q}$	$-\alpha \in A - B \cup \{\tau\}$	$ \begin{array}{c} P \xrightarrow{a} P' \\ Q \xrightarrow{a} Q' \\ \hline P_A \ _B Q \xrightarrow{a} P'_A \ _B Q' \end{array} a \in A \cap B $
$\frac{P \xrightarrow{\alpha} P'}{P \setminus A \xrightarrow{\alpha} P' \setminus A} C$	$\alpha \in \Sigma - A \cup \{\tau\}$	$ \begin{array}{c} P \xrightarrow{a} P' \\ \hline P \setminus A \xrightarrow{\tau} P' \setminus A \end{array} a \in A \end{array} $
$div \xrightarrow{\tau} div$		$\mu X \bullet P \stackrel{\tau}{\longrightarrow} P[\mu X \bullet P/X]$
The following rule	s show how the tests f	or readiness operate.

if ready *a* then *P* else $Q \xrightarrow{\text{ready } a} P$, if ready *a* then *P* else $Q \xrightarrow{\text{notReady } a} Q$.

The remaining rules show how the readiness tests are promoted by various operators. We omit symmetrically equivalent rules for brevity. The rules for the choice operators are straightforward.

The rules for parallel composition are a little more involved. A *ready a* action can occur only if all processes with *a* in their alphabet are able to perform *a*.

A *notReady a* action requires at least one parallel peer with *a* in its alphabet to be unable to perform *a*. In this case, the action is converted into a τ .

$$\frac{P \xrightarrow{\text{notReady a}} P'}{P_A \parallel_B Q \xrightarrow{\text{notReady a}} P'_A \parallel_B Q} a \notin B$$

Note that in the second rule, the *notReady b* may yet be blocked by some other parallel peer.

If a *ready a* action can be performed in a context where *a* is then hidden, then all relevant parallel peers are able to perform *a*; hence the transition can occur; the action is converted into a τ .

$$\begin{array}{c} P \xrightarrow{ready a} P' \\ \hline P \setminus A \xrightarrow{\tau} P' \setminus A \end{array} a \in A \\ \hline P \xrightarrow{\alpha} P' \\ \hline P \setminus A \xrightarrow{\alpha} P' \setminus A \end{array} \alpha \in ready(\Sigma - A) \cup notReady(\Sigma - A) \end{array}$$

Note that there is no corresponding rule for *notReady a*: in the context $P \setminus A$, if P can perform *notReady a* (with $a \in A$) then all parallel peers with a in their alphabet are able to perform a, and so the a is available; hence the *notReady a* action is blocked for $P \setminus A$.

The following two lemmas can be proved using straightforward structural inductions. First, *ready a* and *notReady a* actions are available as alternatives to one another.

Lemma 6 For every process *P*:

$$(\exists Q \bullet P \xrightarrow{ready a} Q) \Leftrightarrow (\exists Q' \bullet P \xrightarrow{notReady a} Q').$$

Informally, the two transitions correspond to taking the two branches of a construct of the form if ready *a* then *R* else R'. The if construct may be only part of the process *P* above, and so *R* and *R'* may be only part of *Q* and *Q'* above.

Initially, each process can perform no standard events. This is a consequence of our assumption that a process of the form $a \rightarrow P$ cannot perform the *a* from its initial state.

Lemma 7 For every process *P* expressible using the syntax of the language (so excluding the $\check{a} \rightarrow \ldots$ construct), and for every standard event $a \in \Sigma$, $P \not\xrightarrow{a}$.

Of course, P might have a τ transition to a state where visible events are available.

5. Denotational semantics

We now consider how to build a compositional denotational semantic model for our language. We want the model to record at least the traces of visible events performed by processes: any coarser model is likely to be trivial.

In order to consider what other information is needed in the model, it is useful to consider (informally) a form of testing: we will say that test *T* distinguishes processes *P* and *Q* if $P \parallel T$ and $Q \parallel T$ have different traces of visible events. In this case, the denotational model should also distinguish *P* and *Q*.

We want to record within traces the *ready* and *notReady* actions that are performed. For example, the processes $b \rightarrow STOP$ and ready $a \& b \rightarrow STOP$ are distinguished by the test *STOP* (with alphabet {a}); we will distinguish them denotationally by including the *ready a* action in the latter's trace.

Further, we want to record the events that were available as alternatives to those events that were actually performed. For example, the processes $a \rightarrow STOP \square b \rightarrow STOP$ and

 $a \rightarrow STOP \sqcap b \rightarrow STOP$ can be distinguished by the test ready $a \& b \rightarrow STOP$; we will distinguish them denotationally by recording that the former offers *a* as an alternative to *b*.

We therefore add actions *offer a* and *notOffer a* to represent that a process is offering or not offering *a*, respectively. These actions will synchronise with *ready a* and *notReady a* actions. We write

$$offer A = \{offer a \mid a \in A\}, \qquad not Offer A = \{not Offer a \mid a \in A\}, \\ A^{\ddagger} = A^{\dagger} \cup offer A \cup not Offer A, \qquad A^{\ddagger\tau} = A^{\ddagger} \cup \{\tau\}.$$

A trace of a process will, then, be a sequence of actions from Σ^{\ddagger} . We can calculate the traces of a process in two ways: by extracting then from the operational semantics, and by giving compositional rules. We begin with the former.

We augment the operational semantics with extra transitions as follows:

- We add *offer a* loops on every state *P* such that $P \xrightarrow{a}$;
- We add *notOffer a* loops on every state P such that $P \neq^{a}$.

Formally, we define a new transition relation —> by:

$$\begin{array}{cccc} P \stackrel{\alpha}{\longrightarrow} Q \iff P \stackrel{\alpha}{\longrightarrow} Q, & \text{for } \alpha \in \Sigma^{\dagger \tau}, \\ P \stackrel{offer \, a}{\longrightarrow} P \iff P \stackrel{a}{\longrightarrow}, \\ P \stackrel{notOffer \, a}{\longrightarrow} P \iff P \stackrel{a}{\longrightarrow}. \end{array}$$

Appendix A gives rules for the \longrightarrow that can be derived from the rules for the \longrightarrow relation and the above definition.

We can then extract the traces (of Σ^{\ddagger} actions) from the operational semantics (following [Ros97, Chapter 7]):

Definition 8 We write $P \stackrel{tr}{\mapsto} Q$, for $tr = \langle \alpha_1, \ldots, \alpha_n \rangle \in (\Sigma^{\ddagger \tau})^*$, if there exist $P_0 = P$, $P_1, \ldots, P_n = Q$ such that $P_i \stackrel{\alpha_{i+1}}{\longrightarrow} P_{i+1}$ for $i = 0, \ldots, n-1$. We write $P \stackrel{tr}{\Longrightarrow} Q$, for $tr \in (\Sigma^{\ddagger})^*$, if there is some tr' such that $P \stackrel{tr'}{\longmapsto} Q$ and $tr = tr' \setminus \tau$.

The traces of process *P* can then be defined to be the set of all *tr* such that $P \stackrel{tr}{\Longrightarrow}$. The following lemma states some healthiness conditions concerning this set.

Lemma 9 For all processes *P* expressible using the syntax of the language (so excluding the $\check{a} \rightarrow \ldots$ construct), the set $T = \{tr \mid P \stackrel{tr}{\Longrightarrow}\}$ satisfies the following conditions:

- 1. *T* is non-empty and prefix-closed.
- 2. *T* includes $(notOffer \Sigma)^*$, i.e., the process starts in a state where no standard events are available.
- 3. offer and notOffer actions can always be remove from or duplicated within a trace:

$$tr^{\langle \alpha \rangle} tr' \in T \Rightarrow tr^{\langle \alpha, \alpha \rangle} tr' \in T \land tr^{\tau} tr' \in T,$$

for $\alpha \in offer \Sigma \cup notOffer \Sigma$.

4. ready a and notReady a actions are available as alternatives to one another:

 $tr^{\langle ready a \rangle} \in T \Leftrightarrow tr^{\langle notReady a \rangle} \in T.$

5. Either an offer a or notOffer a action is always available.

 $tr^{tr'} \in T \Rightarrow tr^{(offer a)^{tr'}} \in T \lor tr^{(notOffer a)^{tr'}} \in T.$

Proof: (Sketch)

- 1. This follows directly from the definition of \implies .
- 2. The follows from Lemma 7 and the definition of --.
- 3. This follows directly from the definition of —>: *offer* and *notOffer* transitions always form self-loops.
- 4. This follows directly from Lemma 6.
- 5. This follows directly from the definition of —>: each state has either an *offer a* or a *notOffer a* loop.

5.1. Compositional traces semantics

We now give compositional rules for the traces of a process. The semantics for each process will be an element of the following model.

Definition 10 The *Readiness-Testing Traces Model* contains those sets $T \subseteq (\Sigma^{\ddagger})^*$ that satisfy conditions 2–5 of Lemma 9.

We write $traces_R[\![P]\!]$ for the traces of P^3 . Below we will show that these are congruent to the operational definition above.

STOP and div are equivalent in this model: they can perform no standard events; they can only signal that they are not offering events.

 $traces_R[STOP]] = traces_R[[div]] = (notOffer \Sigma)^*.$

The process $a \rightarrow P$ can initially signal that it is not offering events; it can then signal that it is offering *a* but not offering other events; it can then perform *a*, and then continue like *P*.

 $traces_{R}\llbracket a \to P \rrbracket =$ $Init \cup \{tr^{\langle a \rangle} tr' \mid tr \in Init \land tr' \in traces_{R}\llbracket P \rrbracket \}$ $where Init = \{tr^{\langle tr'} \mid tr \in (notOffer \Sigma)^{*} \land tr' \in (\{offer a\} \cup notOffer(\Sigma - \{a\}))^{*} \}.$

The process if ready a then P else Q can initially signal that it is not offering events; it can then either detect that a is ready and continue as P, or detect that a is not ready and continue like Q.

$$traces_{R}[\![if ready a then P else Q]\!] = (notOffer \Sigma)^{*} \cup \{tr^{rady a}^{tr'} | tr \in (notOffer \Sigma)^{*} \wedge tr' \in traces_{R}[\![P]\!]\} \cup \{tr^{rady a}^{tr'} | tr \in (notOffer \Sigma)^{*} \wedge tr' \in traces_{R}[\![Q]\!]\}.$$

The process $P \triangleright Q$ can either perform a trace of P, or can perform a trace of P with no standard events, and then (after the timeout) perform a trace of Q. The process $P \sqcap Q$ can perform traces of either of its components.

 $traces_{R}\llbracket P \triangleright Q \rrbracket =$ $traces_{R}\llbracket P \rrbracket \cup \{tr_{P} \ tr_{Q} \mid tr_{P} \in traces_{R}\llbracket P \rrbracket \land tr_{P} \upharpoonright \Sigma = \langle \rangle \land tr_{Q} \in traces_{R}\llbracket Q \rrbracket \},$ $traces_{R}\llbracket P \sqcap Q \rrbracket = traces_{R}\llbracket P \rrbracket \cup traces_{R}\llbracket Q \rrbracket.$

Before the first visible event, the process $P \Box Q$ can perform an *offer a* action if either *P* or *Q* can do so; it can perform a *notOffer a* action if *both P* and *Q* can do so. Therefore, *P*

³We include the subscript "*R*" in $traces_R[[P]]$ to distinguish this semantics from the standard traces semantics, traces[[P]].

and Q must synchronise on all *notOffer* actions before the first visible event. Let $tr = \int_{notOffer \Sigma} tr'$ be the set of ways of interleaving tr and tr', synchronising on all *notOffer* actions (this operator is a specialisation of the $\|$ operator defined in [Ros97, page 70]). The three sets in the definition below correspond to the cases where (a) neither process performs any visible events (so the two processes synchronise on *notOffer* actions throughout the execution), (b) *P* performs at least one visible event (after which, *Q* is turned off), and (c) the symmetric case where *Q* performs at least one visible event.

$$traces_{R}\llbracket P \Box Q \rrbracket = \{tr \mid \exists tr_{P} \in traces_{R}\llbracket P \rrbracket, tr_{Q} \in traces_{R}\llbracket Q \rrbracket \bullet tr_{P} \upharpoonright \Sigma = tr_{Q} \upharpoonright \Sigma = \langle \rangle \land tr \in tr_{P} \parallel tr_{Q} \} \cup tr^{\langle a \rangle} tr'_{P} \mid \exists tr_{P} \land a \rangle tr'_{P} \in traces_{R}\llbracket P \rrbracket, tr_{Q} \in traces_{R}\llbracket Q \rrbracket \bullet tr_{P} \upharpoonright \Sigma = tr_{Q} \upharpoonright \Sigma = \langle \rangle \land a \in \Sigma \land tr \in tr_{P} \parallel tr_{Q} \} \cup \{tr^{\langle a \rangle} tr'_{Q} \mid \exists tr_{P} \in traces_{R}\llbracket P \rrbracket, tr_{Q} \land a \land tr'_{Q} \in traces_{R}\llbracket Q \rrbracket \bullet tr_{P} \upharpoonright \Sigma = tr_{Q} \upharpoonright \Sigma = \langle \rangle \land a \in \Sigma \land tr \in tr_{P} \parallel tr_{Q} \} \cup \{tr^{\langle a \rangle} tr'_{Q} \mid \exists tr_{P} \in traces_{R}\llbracket P \rrbracket, tr_{Q} \land a \land tr'_{Q} \in traces_{R}\llbracket Q \rrbracket \bullet tr_{P} \upharpoonright \Sigma = tr_{Q} \upharpoonright \Sigma = \langle \rangle \land a \in \Sigma \land tr \in tr_{P} \parallel tr_{Q} \}.$$

In order to give a semantic equation for parallel composition, we define a relation to capture how traces of parallel components are combined⁴. We write $(tr_P, tr_Q) \stackrel{|A||_B}{\longrightarrow} tr$ if the traces tr_P of P and tr_Q of Q can lead to the trace tr of $P_A||_BQ$. Let $private_A = (A - B) \cup$ offer $(A - B) \cup notOffer A \cup ready (\Sigma - B) \cup notReady(\Sigma - B)$; these are the actions that the process with alphabet A can perform without any cooperation from the other process. Let $sync_{A,B} = (A \cap B) \cup offer(A \cap B)$; these are the actions that the two processes synchronise upon. The relation $\stackrel{|A||_B}{\longrightarrow}$ is defined by:

$$\begin{split} (\langle \rangle, \langle \rangle) & \stackrel{A \parallel B}{\longmapsto} \langle \rangle, \\ \text{if } (tr_P, tr_Q) & \stackrel{A \parallel B}{\longrightarrow} tr \text{ and } b \in B, \text{ then} \\ (\langle \alpha \rangle^{-} tr_P, tr_Q) & \stackrel{A \parallel B}{\longrightarrow} \langle \alpha \rangle^{-} tr, \quad \text{ for } \alpha \in private_A, \\ (\langle \alpha \rangle^{-} tr_P, \langle \alpha \rangle^{-} tr_Q) & \stackrel{A \parallel B}{\longrightarrow} \langle \alpha \rangle^{-} tr, \quad \text{ for } \alpha \in sync_{A,B}, \\ (\langle ready b \rangle^{-} tr_P, \langle offer b \rangle^{-} tr_Q) & \stackrel{A \parallel B}{\longrightarrow} \langle ready b \rangle^{-} tr, \\ (\langle notReady b \rangle^{-} tr_P, \langle notOffer b \rangle^{-} tr_Q) & \stackrel{A \parallel B}{\longrightarrow} tr, \\ (\langle notReady b \rangle^{-} tr_P, \langle offer b \rangle^{-} tr_Q) & \stackrel{A \parallel B}{\longrightarrow} \langle notReady b \rangle^{-} tr, \\ \langle \text{The symmetric equivalents of the above cases} \rangle. \end{split}$$

In the second clause: the first case corresponds to P performing a private action; the second case corresponds to P and Q synchronising on a shared action; the third case corresponds to a readiness test of P detecting that Q is offering b; the fourth case corresponds to a non-readiness test of P detecting that Q is not offering b; the fifth case corresponds to a non-readiness test of P detecting that Q is offering b. The reader might like to compare this definition with the corresponding operational semantics rules for parallel composition.

The semantics of parallel composition is then as follows; note that each component is restricted to its own alphabet, and that the composition can perform arbitrary *notOffer* $(\Sigma - A - B)$ actions:

⁴One normally defines a set-valued function to do this, but in our case it is more convenient to define a relation, since this leads to a much shorter definition.

 $\begin{aligned} traces_{R}\llbracket P_{A} \Vert_{B}Q \rrbracket &= \\ \{tr \mid \exists tr_{P} \in traces_{R}\llbracket P \rrbracket, tr_{Q} \in traces_{R}\llbracket Q \rrbracket \bullet \\ tr_{P} \upharpoonright (\Sigma - A) \cup offer(\Sigma - A) \cup notOffer(\Sigma - A) = \langle \rangle \land \\ tr_{Q} \upharpoonright (\Sigma - B) \cup offer(\Sigma - B) \cup notOffer(\Sigma - B) = \langle \rangle \land \\ (tr_{P}, tr_{Q}) \stackrel{A \Vert_{B}}{\longrightarrow} tr \setminus notOffer(\Sigma - A - B) \}. \end{aligned}$

The semantic equation for hiding of A captures that *notReadyA* and *offerA* actions are blocked, A and *readyA* actions are internalised, and arbitrary *notOfferA* actions can occur.

 $traces_{R}\llbracket P \setminus A \rrbracket =$ $\{tr \mid \exists tr_{P} \in traces_{R}\llbracket P \rrbracket \bullet tr_{P} \upharpoonright (notReadyA \cup offerA) = \langle \rangle \land$ $tr_{P} \setminus (A \cup readyA) = tr \setminus notOfferA \}.$

We now consider the semantics of recursion. Our approach follows the standard method using complete partial orders; see, for example, [Ros97, Appendix A.1].

Lemma 11 The Readiness-Testing Traces Model forms a complete partial order under the subset ordering \subseteq , with *traces*_R[[div]] as the bottom element.

Proof: That $traces_R[[div]]$ is the bottom element follows from item 2 of Lemma 9. It is straightforward to see that the model is closed under arbitrary unions, and hence is a complete partial order.

The following lemma can be proved using precisely the same techniques as for the standard traces model; see [Ros97, Section 8.2].

Lemma 12 Each of the operators is continuous with respect to the \subseteq ordering.

Hence from Tarski's Theorem, each mapping *F* definable using the operators of the language has a least fixed point given by $\bigcup_{n>0} F^n(\text{div})$. This justifies the following definition.

 $traces_R[\![\mu X \bullet F(X)]\!] =$ the \subseteq -least fixed point of the semantic mapping corresponding to *F*.

The following theorem shows that the two ways of capturing the traces are congruent.

Theorem 13 For all traces $tr \in (\Sigma^{\ddagger})^*$:

 $tr \in traces_R[\![P]\!]$ iff $P \stackrel{tr}{\Longrightarrow}$.

Proof: (Sketch.) By structural induction over the syntax of the language. We give a couple of cases in Appendix B. \Box

Theorem 14 For all processes, $traces_R[\![P]\!]$ is a member of the Readiness-Testing Traces Model (i.e., it satisfies conditions 2–5 of Lemma 9).

Proof: This follows directly from Lemma 9 and Theorem 13.

We can relate the semantics of a process in this model to the standard traces semantics. Let ϕ be the function that replaces readiness tests by nondeterministic choices, i.e.,

 $\phi(\text{if ready } a \text{ then } P \text{ else } Q) = \phi(P) \sqcap \phi(Q)$

and ϕ distributes over all other operators (e.g. $\phi(P_A||_BQ) = \phi(P)_A||_B\phi(Q)$). The standard traces of $\phi(P)$ are just the projection onto standard events of the readiness-testing traces of P.

Theorem 15 $traces[\![\phi(P)]\!] = \{tr \upharpoonright \Sigma \mid tr \in traces_R[\![P]\!]\}.$

5.2. Failures

We now consider how to refine the semantic model, to make it analogous to the stable failures model [Ros97], i.e. to record information about which events can by stably refused.

The refusal of events seems, at first sight, to be very similar to those events not being offered, as recorded by *notOffer* actions. The difference is that refusals are recorded only in stable states, i.e. where no internal events are available: this means that if an event is stably refused, it will continue to be refused (until a visible event is performed); on the other hand, *notOffer* actions can occur in any states, and may subsequently become unavailable. So, for example:

- $a \rightarrow STOP \sqcap STOP$ is equivalent to $a \rightarrow STOP$ in the Readiness-Testing Traces model, since the traces of *STOP* are included in the initial traces of $a \rightarrow STOP$; but $a \rightarrow STOP \sqcap STOP$ can stably refuse *a* initially, whereas $a \rightarrow STOP$ cannot.
- *a* → *STOP* ▷ *STOP* ▷ *a* → *STOP* has the trace ⟨*offer a*, *notOffer a*, *offer a*⟩ (where the *notOffer a* action is from the intermediate *STOP* state) whereas *a* → *STOP* does not; but neither process can stably refuse *a* before *a* is performed.

Recall that in the standard model, stable failures are of the form (tr, X), where tr is a trace and X is a set of events that are stably refused. For the language in this paper, should refusal sets contain actions other than standard events?

Firstly, we should not consider states with *ready* or *notReady* transitions to be stable: recall that we consider these actions to be similar to τ events, in that they are not externally visible. We define:

stable $P \Leftrightarrow \forall \alpha \in ready \Sigma \cup notReady \Sigma \cup \{\tau\} \bullet \neg P \stackrel{\alpha}{\longrightarrow}$.

Therefore such actions are necessarily unavailable in stable states, so there is no need to record them in refusal sets.

There is also no need to record the refusal of an *offer a* action, since this will happen precisely when the event *a* is refused. It turns out that including *notOffer* actions within refusal sets can add to the discriminating power of the model. Consider

$$P = a \rightarrow STOP \triangleright STOP,$$

$$O = (a \rightarrow STOP \triangleright STOP) \sqcap a \rightarrow STOP.$$

Then *P* and *Q* have the same traces, and have the same stable refusals of standard events. However, *Q* can, after the empty trace, stably refuse $\{b, notOffer a\}$ (i.e., stably offer *a* and stably refuse *b*), whereas *P* cannot.

We therefore have a choice as to whether or not we include *notOffer* actions within refusal sets. We choose not to, because the distinctions one can make by including them do not seem useful, and excluding them leads to a simpler model: in particular, the refusal of *notOffer* actions do not contribute to the performance or refusal of any standard events. I suspect that including *notOffer* actions within refusal sets would lead to a model similar in style to the stable ready sets model [OH86,Ros09a].

Hence, we define, for $X \subseteq \Sigma$:

 $P \text{ ref } X \Leftrightarrow stable P \land \forall x \in X \bullet \neg P \stackrel{x}{\longrightarrow} .$

We then define the stable failures of a process in the normal way:

$$(tr, X) \in failures_{R}[\![P]\!] \Leftrightarrow \exists Q \bullet P \stackrel{tr}{\Longrightarrow} Q \land Q \text{ ref } X.$$

$$(2)$$

Definition 16 The Readiness-Testing Stable Failures Model contains those pairs (T, F) where $T \subseteq (\Sigma^{\ddagger})^*$, $F \subseteq (\Sigma^{\ddagger})^* \times \mathbf{P}\Sigma$, T satisfies conditions 2–5 of the Readiness-Testing Traces Model, and also

6. If $(tr, X) \in F$ then $tr \in T$.

Below, we give compositional rules for the stable failures of a process. Since the notion of refusal is identical to as in the standard stable failures model, the refusal components are calculated precisely as in that model, and so the equations are straight-forward adaptations of the rules for traces. The only point worth noting is that in the construct if ready *a* then P else Q, no failures are recorded before the if is resolved.

 $failures_R[[div]] = \{\},\$ $failures_{R}[[STOP]] = \{(tr, X) \mid tr \in (notOffer \Sigma)^{*} \land X \subseteq \Sigma\},\$ failures_R $[a \rightarrow P] =$ $\{(tr, X) \mid tr \in Init \land a \notin X\} \cup$ $\{(tr^{\langle a \rangle} tr', X) \mid tr \in Init \land (tr', X) \in failures_{R}[\![P]\!]\}$ where $Init = \{tr \ tr' \mid tr \in (notOffer \ \Sigma)^* \land tr' \in (\{offer \ a\} \cup notOffer(\Sigma - \{a\}))^*\},\$ $failures_R$ [[if ready a then P else Q]] = $\{(tr^{rady a}^{tr'}, X) \mid tr \in (notOffer \Sigma)^* \land (tr', X) \in failures_{\mathbb{R}}[[P]]\} \cup$ { $(tr^{(notReady a)^{tr'}, X) \mid tr \in (notOffer \Sigma)^* \land (tr', X) \in failures_R[[Q]]}$ } $failures_R[\![P \triangleright Q]\!] =$ $\{(tr, X) \mid (tr, X) \in failures_R[[P]] \land tr \upharpoonright \Sigma \neq \langle \rangle \} \cup$ $\{(tr_P \uparrow tr_Q, X) \mid tr_P \in traces[[P]] \land tr_P \upharpoonright \Sigma = \langle \rangle \land (tr_Q, X) \in failures_R[[Q]] \},\$ $failures_{R}[\![P \sqcap Q]\!] = failures_{R}[\![P]\!] \cup failures_{R}[\![Q]\!],$ failures_R $[P \Box Q] =$ $\{(tr, X) \mid \exists (tr_P, X) \in failures_R[\![P]\!], (tr_Q, X) \in failures_R[\![Q]\!] \bullet$ $tr_{P} \upharpoonright \Sigma = tr_{Q} \upharpoonright \Sigma = \langle \rangle \land tr \in tr_{P} \parallel_{notOffer \Sigma} tr_{Q} \} \cup$ $\{(tr^{\langle a \rangle} tr'_{P}, X) \mid$ $\begin{array}{l} \langle a \rangle \ tr_{P}, \Lambda \rangle \mid \\ \exists (tr_{P}^{\wedge} \langle a \rangle^{\wedge} tr'_{P}, X) \in failures_{R}[\![P]\!], tr_{Q} \in traces[\![Q]\!] \bullet \\ tr_{P} \upharpoonright \Sigma = tr_{Q} \upharpoonright \Sigma = \langle \rangle \wedge a \in \Sigma \wedge tr \in tr_{P} \quad \| \quad tr_{Q} \rbrace \cup \\ notOffer \Sigma \end{array}$ $\{(tr^{\langle a \rangle} tr'_{O}, X) \mid$ $\exists tr_{P} \in traces[[P]], (tr_{Q}^{\wedge}\langle a \rangle^{\wedge} tr'_{Q}, X) \in failures_{R}[[Q]] \bullet$ $tr_{P} \upharpoonright \Sigma = tr_{Q} \upharpoonright \Sigma = \langle \rangle \land a \in \Sigma \land tr \in tr_{P} || tr_{Q} \},$ $failures_R[P_A||_BQ] =$ $\{(tr, Z) \mid \exists (tr_P, X) \in failures_R[P], (tr_O, Y) \in failures_R[Q]] \bullet$ $tr_P \upharpoonright (\Sigma - A) \cup offer(\Sigma - A) \cup notOffer(\Sigma - A) = \langle \rangle \land$ $tr_{O} \upharpoonright (\Sigma - B) \cup offer(\Sigma - B) \cup notOffer(\Sigma - B) = \langle \rangle \land$ $(tr_P, tr_O) \stackrel{A \parallel_B}{\longmapsto} tr \setminus notOffer(\Sigma - A - B) \land Z - A - B = X \cap A \cup Y \cap B\},\$ $failures_R[\![P \setminus A]\!] =$ $\{(tr, X) \mid \exists (tr_P, X \cup A) \in failures_R[\![P]\!] \bullet$ $tr_P \upharpoonright (notReadyA \cup offerA) = \langle \rangle \land tr_P \setminus (A \cup readyA) = tr \setminus notOfferA \},$ failures_R[[$\mu X \bullet F(X)$]] =

the \subseteq -least fixed point of the semantic mapping corresponding to *F*.

The fixed-point definition for recursion can be justified in a similar way to as for traces. The congruence of the above rules to the operational definition of stable failures —i.e., equation (2)— can be proved in a similar way to Theorem 13. Conditions 2–5 of the Readiness-Testing Stable Failures Model are satisfied, because of the corresponding result for traces

(Theorem 14). Condition 6 follows directly from the definition of a stable failure, and the congruence of the operational and denotational semantics.

The following theorem relates the semantics of a process in this model to the standard stable failures semantics.

Theorem 17 failures $\llbracket \phi(P) \rrbracket = \{ (tr \upharpoonright \Sigma, X) \mid (tr, X) \in failures_R \llbracket P \rrbracket \}.$

6. Model checking

In this section we illustrate how one can use a standard CSP model checker, such as FDR [Ros94,For97], to analyse processes in the extended language of this paper. We just give an example here, in order to give the flavour of the translation; we discuss prospects for generalising the approach in the concluding section of the paper.

We consider the following solution to the readers and writers problem.

 $\begin{aligned} Guard(r,w) &= \\ w &= 0 \land r < N \& \text{ notReady } startWrite \& startRead \rightarrow Guard(r+1,w) \\ \Box r &> 0 \& endRead \rightarrow Guard(r-1,w) \\ \Box r &= 0 \land w = 0 \& startWrite \rightarrow Guard(r,w+1) \\ \Box w &> 0 \& endWrite \rightarrow Guard(r,w-1). \end{aligned}$

This is the solution from Section 3 that gives priority to writers, except we impose a bound of N upon the number of readers, and add guards to the second and fourth branches, in order to keep the state space finite. We will show that this solution is starvation free as far as the writers is concerned: i.e. if a writer is trying to gain access then one such writer eventually succeeds.

We will simulate the above guard process using standard CSP, in particular simulating the *ready*, *notReady*, *offer* and *notOffer* actions by fresh CSP events on channels ready, notReady, offer and notOffer. Each process is translated into a form that uses these channels, following the semantics presented earlier; the simulation will have transitions that correspond to the \longrightarrow transitions of the original, except it will have a few additional τ transitions that do not affect the failures-divergences semantics. More precisely, let $\hat{\alpha}$ be the event used to simulate the action α ; for example, if $\alpha = ready e$ then $\hat{\alpha} = ready.e$. Then each process *P* is simulated by a translation *trans*(*P*), where if $P \xrightarrow{\alpha} Q$ then *trans*(*P*) $\xrightarrow{\hat{\alpha}} (\xrightarrow{\tau})^* trans(Q)$, and vice versa. In particular, for each standard event *e*, we must add an offer.*e* or notOffer.*e* loop to each state.

For convenience, and to distinguish between the source and target languages, we present the simulation using prettified machine-readable CSP.⁵

The standard events and the channels to simulate the non-standard actions are declared as follows:

```
channel startWrite, endWrite, startRead, endRead
E = {startWrite, endWrite, startRead, endRead}
channel ready, notReady, offer, notOffer : E
```

We start by defining some helper processes. The following process is the translation of *STOP*: it can only signal that it is not offering standard events.

$\texttt{STOPT} = \texttt{notOffer?e} \rightarrow \texttt{STOPT}$

⁵The CSP text below is produced (almost) directly from the machine-readable CSP using LATEX macros.

The following process is the translation of $e \rightarrow P$: initially it can signal that it is not offering standard events; it can then timeout into a state where e is available, after which it acts like P; in this latter state it can also signal that it is offering e but no other standard events.⁶

```
Prefix (e,P) = notOffer?e1 \rightarrow Prefix (e,P) \triangleright Prefix1 (e,P)

Prefix1 (e,P) =

e \rightarrow P

\Box offer.e \rightarrow Prefix1 (e,P)
```

```
□ notOffer?e1: diff (E, {e}) \rightarrow Prefix1 (e, P)
```

The reader might like to compare these with the \longrightarrow semantics in Appendix A.

In order to simulate the *Guard* process, we simulate each branch as a separate parallel process: the branches of *Guard* synchronise on *notOffer* actions before the choice is resolved, so the processes simulating these branches will synchronise on appropriate notOffer events. The first branch is simulated as below:

```
Branch(1,r,w) =

if w==0 and r<N then

notReady.startWrite → Prefix(startRead, Restart(1,r+1,w))

□ ready.startWrite → STOPT

□ notOffer?e → Branch(1,r,w)

else STOPT
```

We explain the Restart process below. Note how the notReady *startWrite* test is simulated by the notReady.startWrite and ready.startWrite events. Note also how the process signals which standard events are and are not available in the different states. The other branches are slightly simpler, as they do not include readiness tests.

```
Branch(2,r,w) =
    if r>0 then Prefix(endRead, Restart(2,r-1,w)) else STOPT
Branch(3,r,w) =
    if r==0 and w==0 then Prefix(startWrite, Restart(3,r,w+1)) else STOPT
Branch(4,r,w) =
    if w>0 then Prefix(endWrite, Restart(4,r,w-1)) else STOPT
```

When one branch executes and reaches a point corresponding to a recursion within the *Guard* process, all the other branch processes need to be restarted, with new values for r or w. We implement this by the executing branch signalling on the channel restart.

```
R = \{0..N\} -- possible values of r
W = \{0..1\} -- possible values of w
BRANCH = \{1..4\} -- branch identifiers
channel restart : BRANCH.R.W
Restart(i,r,w) = restart!i.r.w \rightarrow Branch(i,r,w)
```

Each branch can receive such a signal from another branch, as an interrupt, and restart with the new values for r and w.⁷

Branch'(i,r,w) = Branch(i,r,w) △ restart?j:diff(BRANCH,{i})?r'.w' → Branch'(i,r',w')

⁶The operator diff represents set difference.

⁷The \triangle is an interrupt operator; the left hand side is interrupted when the right hand side performs an event.

Below we will combine these Branch' processes in parallel so as to simulate *Guard*. We will need to be able to identify which branch performs certain events. For events e other than notOffer events, we rename e performed by branch i to c.i.e. We rename each notOffer.e event performed by branch i to both itself and notOffer1.i.e: the former will be used before the choice is resolved (synchronised between all branch processes), and the latter will be used after the choice is resolved (privately to branch i):⁸

```
EE = union(E, {|ready, notReady, offer}) -- events other than notOffer
channel c : BRANCH.EE -- c.i.e represents event e done by Branch(i,_,_)
channel notOffer1 : BRANCH.E
Branch'(i,r,w) =
Branch'(i,r,w)
        [e \ c.i.e | e ← EE]]
        [notOffer.e \ notOffer.e, notOffer.e \ notOffer1.i.e | e ← E]]
```

```
alpha(i) = {|c.i, restart, notOffer, notOffer1.i|} -- alphabet of branch i
```

Below we will combine the branch processes in parallel, together with a regulator process Reg that, once a branch has done a standard event to resolve the choice, blocks all events of the other branches until a restart occurs; further, it forces processes to synchronise on notOffer events before the choice is resolved, and subsequently allows the unsynchronised notOffer1 events.⁹

```
Reg = c?i?e → (if member(e,E) then Reg'(i) else Reg)
□ notOffer?_ → Reg
Reg'(i) = c.i?_ → Reg'(i) □ restart.i?_?_ → Reg □ notOffer1.i?_ → Reg'(i)
```

We build the guard process by combining the branches and regulator in parallel, hiding the restart events, and reversing the above renaming.¹⁰

```
Guard0(r,w) =

(|| i : BRANCH • [alpha(i)] Branch''(i,r,w))

[| {|c,restart,notOffer,notOffer1|} |] Reg

Guard(r,w) =

(Guard0(r,w) \ {| restart |})

[[c.i.e \ e | e ← EE, i ← BRANCH]]

[[notOffer1.i.e \ notOffer.e | e ← E, i ← BRANCH]]
```

We now model the readers and writers themselves. Each reader alternates between performing startRead and endRead. Each writer is similar, but, for later convenience, we add an event writerTrying to indicate that it is trying to perform a write.

```
channel writerTrying
```

⁸union represents the union operation.

⁹member tests for membership of a set.

¹⁰The || is an indexed parallel composition, indexed by i; here the ith component has alphabet alpha(i). The notation || A || is the machine-readable CSP version of ||.

We need to synchronise the offer.startWrite and notOffer.startWrite events of the writers with the ready.startWrite and notReady.startWrite events of the guard, respectively. For convenience, we block the remaining offer and notOffer events, since we make no use of them, and processes do not change state when they perform such an event.

We can check the simple safety property that the guard allows at most one active writer at a time, and never allows both readers and writers to be active.

This test succeeds, at least for small values of N.

More interestingly, we can also verify that the guard is fair to the writers, in the sense that if at least one writer is trying to gain access, then one of them eventually succeeds. The parameter n of WLSpec0, below, records how many writers are currently trying; when n>0 (i.e. at least one writer is trying), this process insists that a writer can start.

```
assert WLSpec(0) \sqsubseteq_{FD} System'
```

The above refinement test succeeds. In particular, this means that the right hand side is divergence-free, so that the startWrite events will become available after a finite number of the hidden events. (Note though that we keep the notReady.startWrite visible, to avoid a divergence that would be caused by an infinite repetition of notReady.startWrite, startRead, endRead events, when no writer was trying.)

7. Discussion

In this paper we have considered an extension of CSP that allows processes to test whether an event is available. We have formalised this construct by giving an operational semantics and congruent denotational semantic models. We have illustrated how we can use a standard model checker to analyse systems in this extended language. In this final section we discuss some related work and possible extensions to this work.

7.1. Comparison with standard models

There have been several different denotational semantic models for CSP. Most of these are based on the standard syntax of CSP, with the standard operational semantics. It is not possible to compare these models directly with the models in this paper, since we have used a more expressive language, with the addition of readiness tests; however, we can compare them with the sub-language excluding this construct.

For processes that do not use readiness tests, the two models of this paper are more distinguishing than the standard traces and stable failures models, respectively. Theorems 15 and 17 show that our models make at least as many distinctions as the standard models. They distinguish processes that the standard models identify, such as

 $a \rightarrow STOP \sqcap (a \rightarrow STOP \triangleright b \rightarrow STOP)$ and $a \rightarrow STOP \sqcap b \rightarrow STOP$:

the former, but not the latter, has the trace $\langle offer a, b \rangle$.

In [Ros09a], Roscoe gives a survey of the denotational models based on the standard syntax. The most distinguishing of those models based on finite observations (and so not modelling divergences) is the *finite linear model*, \mathcal{FL} . This model uses observations of the form $\langle A_0, a_0, A_1, a_1, \ldots, a_{n-1}, A_n \rangle$, where each a_i is an event that is performed, and each A_i is either (a) a set of events, representing that those events are offered in a stable state (and so $a_i \in A_i$), or (b) the special value \bullet representing no information about what events are stably offered (perhaps because the process did not stabilise).

For processes with no readiness tests, \mathcal{FL} is incomparable with the models in this paper. Our models distinguish processes that \mathcal{FL} identifies, essentially because the latter records the availability of events only in stable states, whereas our models record this information also in unstable states. For example, the processes

$$(b \rightarrow STOP \triangleright a \rightarrow STOP) \sqcap b \rightarrow STOP$$
 and $a \rightarrow STOP \sqcap b \rightarrow STOP$

are distinguished in our models since just the former has the trace $\langle offer b, a \rangle$; however they are identified in \mathcal{FL} (and hence all the other finite observation models from [Ros09a]) because this *b* is not *stably* available.

Conversely, \mathcal{FL} distinguishes processes that our models identify, such as

$$a \to b \to STOP \sqcap (a \to STOP \triangleright STOP)$$
 and $a \to (b \to STOP \sqcap STOP) \triangleright STOP$,

since the former has the observation $\langle \{a\}, a, \bullet, b, \bullet \rangle$, but the latter does not since its *a* is performed from an unstable state; however, they are identified by our failures model (and hence our traces model) since this records stability information only at the end of a trace. I believe it would be straightforward to extend our models to record stability information *throughout* the trace in the way \mathcal{FL} does.

Roscoe also shows that each of the standard finite observation models can be extended to model divergences in three essentially different ways. I believe that it would be straightforward to extend the model in this paper to include divergences following any of these techniques.

7.2. Comparison with other prioritised models

As we described in the introduction, the readiness tests can be used to implement a form of priority. There have been a number of previous attempts to add priority to CSP.

Lawrence [Law04] models priorities by representing a process as a set of triples of the form (tr, X, Y), meaning that after performing trace tr, if a process is offered the set of

events *X*, then it will be willing to perform any of the events from *Y*. For example, a process that initially gives priority to *a* over *b* would include the triple $\langle \rangle$, $\{a, b\}$, $\{a\}$).

Fidge [Fid93] models priorities using a set of "preferences" relations over events. For example, a process that gives priority to *a* over *b* would have the preferences relation $\{a \mapsto a, b \mapsto b, a \mapsto b\}$.

In [Low93,Low95], I modelled priorities within timed CSP using an order over the sets (actually, multi-sets) of events that the process could do at the same time. For example, a process that gives priority to *a* over *b* (and would rather do either than nothing) at time 0 would have the ordering $(0, \{a\}) \supseteq (0, \{b\}) \supseteq (0, \{\})$.

All the above models are rather complex: I would claim that the model in this paper is somewhat simpler, and has the advantage of allowing a translation into standard CSP, in order to use the FDR model checker.

One issue that sometimes arises when considering priority is what happens when two processes with opposing priorities are composed in parallel. For example, consider $P \parallel Q$ where *P* and *Q* give priority to *a* and *b* respectively:

 $P = a \rightarrow P_1 \Box \text{ notReady } a \& b \rightarrow P_2,$ $Q = b \rightarrow Q_1 \Box \text{ notReady } b \& a \rightarrow Q_2.$

There are essentially three ways in which this parallel composition can behave (in a context that doesn't block a or b):

- If *P* performs its notReady test before *Q*, the test succeeds; if, further, *P* makes the *b* available before *Q* performs its notReady test, then *Q*'s notReady test will fail, and so the parallel composition will perform *b*;
- The symmetric opposite of the previous case, where Q performs its notReady test and makes a available before P performs its notReady test, and so the parallel composition performs a;
- If both processes perform their notReady tests before the other process makes the corresponding event available, then both tests will succeed, and so both events will be possible.

I consider this to be an appropriate solution. By contrast, the model in [Law04] leads to this system deadlocking; in [Low93,Low95] I used a prioritised parallel composition operator to give priority to the preferences of one components, thereby avoiding this problem, but at the cost of considerable complexity.

7.3. Readiness testing for channels

Most CSP-like programming languages make use of channels that pass data. In such languages, one can often test for the availability of *channels*, rather than of individual events. Note, though, that there is an asymmetry between the reader and writer of the channel: the reader will normally want to test whether there is *any* event on the channel that is ready for communication (i.e. the writer is ready to communicate), whereas the writer will normally want to test whether *all* the events on the channel are ready for communication (i.e. the reader is ready to communicate). It would be interesting to extend the language of this paper with constructs

if ready all A then P else Q and if ready any A then P else Q,

where A is a set of events (e.g. all events on a channel), to capture this idea.

7.4. Model checking

In Section 6, we gave an indication as to how to simulate the language of this paper using standard CSP, so as to use a model checker such as FDR.

This technique works in general. This can be shown directly, by exhibiting the translation. Alternatively, we can make use of a general result from [Ros09b], where Roscoe shows that any operator with an operational semantics that is "CSP-like" —essentially that the operator can turn arguments on, interact with arguments via visible events, promote τ events of arguments, and maybe turn arguments off— can be simulated using standard CSP operators. The —> semantics of this paper is "CSP-like" in this sense, so we can use those techniques to simulate this semantics. We intend to automate the translation.

One difficulty is that the translation from [Ros09b] can produce processes that are infinite state because they branch off extra parallel processes at each recursion of the simulated process. In Section 6 we avoided this problem by *restarting* the Branch processes that constituted the guard at each recursion (this uses an idea also due to Roscoe), whereas Roscoe's approach would branch off new processes at each recursion. I believe the technique in Section 6 can be generalised and probably automated.

7.5. Full abstraction

The denotational semantic models we have presented turn out not to be fully abstract with respect to may-testing [dNH84]. Consider the process if ready a then P else P. This is denotationally distinct from P, since its traces have *ready* a or *notReady* a events added to the traces of P. Yet there seems no way to distinguish the two processes by testing: i.e., there is no good reason to consider those processes as distinct.

We believe that one could form a fully abstract semantics as follows. Consider the relation \sim over sets of traces defined by

$$(S \cup \{tr^{tr'}\}) \sim (S \cup \{tr^{dready} a^{tr'}, tr^{dready} a^{tr'}\})$$

for all $S \in \mathbf{P}(\Sigma^*), tr, tr' \in \Sigma^*, a \in \Sigma.$

In other words, two sets are related if one is formed from the other by adding *ready a* and *notReady a* actions in the same place. Let \approx be the transitive reflexive closure of \sim . This relation essentially abstracts away irrelevant readiness tests. We conjecture that *P* and *Q* are testing equivalent iff *traces*[[*P*]] \approx *traces*[[*Q*]], and that it might be possible to produce a compositional semantics corresponding to this equivalence. It is not clear that the benefits of full abstraction are worth this extra complexity, though.

Acknowledgements

I would like to think Bill Roscoe and Bernard Sufrin for interesting discussions on this work. I would like to thank the anonymous referees for a number of very useful suggestions.

References

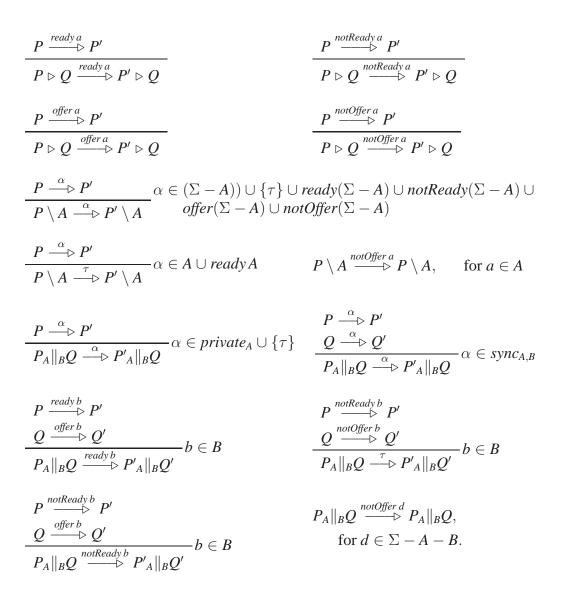
- [And00] Gregory R. Andrews. *Foundations of Multithreaded, Parallel, and Distributed Programming*. Addison-Wesley, 2000.
- [CHP71] P. J. Courtois, F. Heymans, and D. L. Parnas. Concurrent control with "readers" and "writers". *Communications of the ACM*, 14(10):667–668, 1971.
- [dNH84] R. de Nicola and M. C. B. Hennessy. Testing equivalences for processes. *Theoretical Computer Science*, 34:83–133, 1984.
- [Fid93] C. J. Fidge. A formal definition of priority in CSP. ACM Transactions on Programming Languages and Systems, 15(4):681–705, 1993.

[For97]	Formal Systems (Europe) Ltd. <i>Failures-Divergence Refinement—FDR 2 User Manual</i> , 1997.
[Hoa85]	C. A. R. Hoare. Communicating Sequential Processes. Prentice Hall, 1985.
[Law04]	A. E. Lawrence. Triples. In <i>Proceedings of Communicating Process Architectures</i> , pages 157–184, 2004.
[Low93]	Gavin Lowe. Probabilities and Priorities in Timed CSP. DPhil thesis, Oxford, 1993.
[Low95]	Gavin Lowe. Probabilistic and prioritized models of Timed CSP. <i>Theoretical Computer Science</i> , 138:315–352, 1995.
[OH86]	E. R. Olderog and C. A. R. Hoare. Specification-oriented semantics for communicating processes. <i>Acta Informatica</i> , 23(1):9–66, 1986.
[Ros94]	A. W. Roscoe. Model-checking CSP. In A Classical Mind, Essays in Honour of C. A. R. Hoare. Prentice-Hall, 1994.
[Ros97]	A. W. Roscoe. The Theory and Practice of Concurrency. Prentice Hall, 1997.
[Ros09a]	A. W. Roscoe. Revivals, stuckness and the hierarchy of CSP models. <i>Journal of Logic and Algebraic Programming</i> , 78(3):163–190, 2009.
[Ros09b]	A.W. Roscoe. On the expressiveness of CSP. Available via http://web.comlab.ox.ac.uk//files/1383/complete(3).pdf, 2009.
[WB09]	Peter Welch and Neil Brown. Communicating sequential processes for Java (JCSP). http://www.cs.kent.ac.uk/projects/ofa/jcsp/,2009.

[WBM⁺07] Peter Welch, Neil Brown, James Morres, Kevin Chalmers, and Bernhard Sputh. Integrating and extending JCSP. In *Communicating Process Architectures*, pages 48–76, 2007.

A. Derived operational semantics

The definition of the \longrightarrow relation, and the operational semantic rules for the \longrightarrow relation can be translated into the following defining rules for \longrightarrow .



B. Congruence of the operational semantics

In this appendix, we prove some of the cases in the proof of Theorem 13:

$$tr \in traces_R[\![P]\!]$$
 iff $P \stackrel{tr}{\Longrightarrow}$.

Hiding

We prove the case of hiding in Theorem 13 using the derived rules in Appendix A.

(\Rightarrow) Suppose $tr \in traces_R[[P \setminus A]]$. Then there exists some $tr_P \in traces_R[[P]]$ such that $tr_P \upharpoonright (notReadyA \cup offerA) = \langle \rangle$ and $tr_P \setminus (A \cup readyA) = tr \setminus notOfferA$. By the inductive hypothesis, $P \xrightarrow{tr_P}$, i.e., $P \xrightarrow{\alpha_1} \rhd \ldots \xrightarrow{\alpha_n} \bowtie$ for some $\alpha_1, \ldots, \alpha_n$ such that $tr_P = \langle \alpha_1, \ldots, \alpha_n \rangle \setminus \{\tau\}$. From the derived operational semantics rules, $P \setminus A$ has the same transitions but with each $\alpha_i \in A \cup readyA$ replaced by a τ , i.e., transitions corresponding to the trace $tr_P \setminus (A \cup readyA)$. Further, using the third derived rule for hiding, arbitrary *notOfferA* self-loops can be added to the transitions, giving transitions corresponding to trace tr. Hence $P \setminus A \xrightarrow{tr}$.

(\Leftarrow) Suppose $P \setminus A \stackrel{tr}{\Longrightarrow}$. Consider the transitions of P that lead to this trace. By consideration of the derived rules, we see that $P \stackrel{tr_P}{\Longrightarrow}$ for some trace tr_P such that $tr_P \upharpoonright (notReadyA \cup offerA) = \langle \rangle$ and $tr_P \setminus (A \cup readyA) = tr \setminus notOfferA$. By the inductive hypothesis, $tr_P \in traces_R[\![P]\!]$. Hence, $tr \in traces_R[\![P \setminus A]\!]$.

Parallel composition

We prove the case of parallel composition in Theorem 13 using the derived rules from Appendix A.

(\Rightarrow) Suppose $tr \in traces_R[\![P_A|\!]_BQ]\!]$. Then there exist $tr_P \in traces_R[\![P]\!]$ and $tr_q \in traces_R[\![Q]\!]$ such that $tr_P \upharpoonright (\Sigma - A) \cup offer(\Sigma - A) \cup notOffer(\Sigma - A) = \langle \rangle, tr_Q \upharpoonright (\Sigma - B) \cup offer(\Sigma - B) \cup notOffer(\Sigma - B) = \langle \rangle$ and $(tr_P, tr_Q) \stackrel{A||_B}{\longrightarrow} tr \backslash notOffer(\Sigma - A - B)$. By the inductive hypothesis, $P \stackrel{tr_P}{\Longrightarrow}$ and $Q \stackrel{tr_Q}{\Longrightarrow}$. So $P \stackrel{\alpha_1}{\longrightarrow} \dots \stackrel{\alpha_n}{\longrightarrow}$ and $Q \stackrel{\beta_1}{\longrightarrow} \dots \stackrel{\beta_m}{\longrightarrow}$ for some $\alpha_1, \dots, \alpha_n, \beta_1, \dots, \beta_m$ such that $tr_P = \langle \alpha_1, \dots, \alpha_n \rangle \setminus \{\tau\}$ and $tr_q = \langle \beta_1, \dots, \beta_m \rangle \setminus \{\tau\}$. We then have that $P_A ||_B Q \stackrel{tr \backslash notOffer(\Sigma - A - B)}{\Longrightarrow}$, since each event implied by $(tr_P, tr_Q) \stackrel{A||_B}{\longrightarrow} tr \setminus notOffer(\Sigma - A - B)$ has a corresponding transition implied by the operational semantics rules (formally, this is a case analysis over the clauses of $\stackrel{A||_B}{\longrightarrow}$, combined with a straightforward induction on m + n). Further, using the final derived rule for parallel composition, arbitrary $notOffer(\Sigma - A - B)$ self-loops can be added to the transitions, giving transitions corresponding to trace tr. Hence $P_A ||_B Q \stackrel{tr}{\longrightarrow}$.

(\Leftarrow) Suppose $P_A ||_B Q \stackrel{tr}{\Longrightarrow}$. Then by item 3 of Lemma 9, $P_A ||_B Q \stackrel{tr \setminus notOffer(\Sigma - A - B)}{\Longrightarrow}$. Consider the transitions of P and Q that lead to this trace according to the operational semantics rules, say $P \stackrel{\alpha_1}{\Longrightarrow} \ldots \stackrel{\alpha_n}{\Longrightarrow}$ and $Q \stackrel{\beta_1}{\Longrightarrow} \ldots \stackrel{\beta_m}{\Longrightarrow}$. Let $tr_P = \langle \alpha_1, \ldots, \alpha_n \rangle \setminus \{\tau\}$ and $tr_q = \langle \beta_1, \ldots, \beta_m \rangle \setminus \{\tau\}$; so $P \stackrel{tr_P}{\Longrightarrow}$ and $Q \stackrel{tr_Q}{\Longrightarrow}$. By the inductive hypothesis, $tr_P \in traces_R[\![P]\!]$ and $tr_Q \in traces_R[\![Q]\!]$. Also, by consideration of the operational semantics rules, $tr_P \upharpoonright (\Sigma - A) \cup offer(\Sigma - A) \cup otoOffer(\Sigma - A) = \langle \rangle$ and $tr_Q \upharpoonright (\Sigma - B) \cup offer(\Sigma - B) \cup notOffer(\Sigma - B) = \langle \rangle$. Further, $(tr_P, tr_Q) \stackrel{A \mid B}{\Longrightarrow} tr \setminus notOffer(\Sigma - A - B)$, since each transition implied by the operational semantics rules has a corresponding event implied by the definition of $\stackrel{A \mid B}{\longrightarrow}$ (formally, this is a case analysis over the operational semantics rules, combined with a straightforward induction on m + n). Hence $tr \in traces_R[\![P_A] |_B Q]$].